

Course Name: Fundamentals of Automation Engineering (BES202)

This course is aimed at building key technical competencies needed by automation engineers. It is focused on basic knowledge and critical understanding of different technologies in the design and maintenance of automation systems.

The course is divided into 4 units:

Unit 1: Introduction to Electrical Engineering

Unit 2: Introduction to Automation Engineering and Control Systems

Unit 3: Introduction to Digital Circuits

Unit 4: Introduction to Instrumentation and Embedded Systems

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Course Home

Grades
People
Pages
Files
Syllabus
Outcomes
Rubrics
Quizzes
Modules
Conferences
Collaborations
Attendance
New Analytics
Settings

Marks - Unit 2
Dear students, Your marks for Unit 2 have been updated in Canva...
Posted on: Apr 29, 2019 at 7:39pm

Assignment - Dynamical models 2
Dear students, Please first read this letter carefully: letter.pdf The...
Posted on: Apr 17, 2019 at 11:51am

Marks - Project Unit 2 - Material Handling Machine
Dear students, In this link you can download your marks for Proj...
Posted on: Apr 11, 2019 at 6:25pm

Collapse All View Progress

Module III Introduction to Digital Circuits

- CD4011 NAND Gate.pdf
- Handout 1 & Activity 1 on Logic Gates.pdf
- Handout 2 on SOP & POS minimization-1.pdf
- Pre-requisites.docx.pdf

Content (Example)

The screenshot displays a Canvas LMS interface. On the left is a navigation sidebar with icons for Account, Dashboard, Courses, Calendar, Inbox, History, Commons, and Help. The main content area shows a PDF viewer for 'Handout 1 & Activity 1 on Logic Gates.pdf'. The PDF content includes the JKLU logo, the title 'Introduction to Digital Logic Gates', a learning objective, a paragraph about Boolean functions, and a reference to a textbook. The PDF viewer has a toolbar with navigation and zoom controls.

Handout 1 & Activity 1 on Logic Gates.pdf

[Download Handout 1 & Activity 1 on Logic Gates.pdf](#) (165 KB)

Page < 1 > of 4

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Introduction to Digital Logic Gates

Learning objective: Evaluate and simplify Boolean functions and implement the minimized logic using logic gates [LO1].

Boolean function are expressed in terms of logic gates. The logic gates: AND, OR, NAND, NOR, Ex-OR, NOT are available in form of integrated circuits. Of these gates the NAND and NOR gates are called the universal gates as all other logic functions are implemented using either of these gates.

The truth table and symbols of these gates is as shown below (please also refer to page 52 of text book: *Digital Logic and Computer Design by M. Morris Mano, Pearson Education.*

◀ Previous Next ▶

Project

The screenshot displays a Canvas LMS interface for a project charter. The left sidebar is similar to the previous screenshot. The main content area shows a 'Project charter' document with a 'Published' status. The document content lists seven items to be included in the charter: Title, Team, Objective and scope, Requirements and constraints, Milestones, Deliverables, and Sign-off.

Project charter

Published

Use these templates:

- [U2 - Activity Sheet 2.docx](#) ↓
- [Project charter.doc](#) ↓
- [Minimize File Preview](#)

Project charter

1. Title:
2. Team:
3. Objective and scope:
4. Requirements and constraints:
5. Milestones:
6. Deliverables:
7. Sign-off